

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	2004/0103234	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:23
L2	2	"20040103234"	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:24
L3	1149	(@ad<"20021009" @rlad<"20021009") and card and DMA with mode	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:30
L4	59	(@ad<"20021009" @rlad<"20021009") and card with DMA with modes	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:30
L5	4	4 and descriptor and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:30
L6	134	(@ad<"20021009" @rlad<"20021009") and DMA with no\$1 with support \$2	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:31
L7	44	6 and data adj structure	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:32
L8	14	7 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:32
L9	1477	(@ad<"20021009" @rlad<"20021009") and DMA with register with bit\$1	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:37

L10	303	9 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:38
L11	624	9 and DMA with enable	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:39
L12	61	10 and DMA adj3 enable	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:39
L13	4	12 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:39
L14	1328	(@ad<"20021009" @rlad<"20021009") and DMA with capable	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:40
L15	17	(@ad<"20021009" @rlad<"20021009") and DMA with capable with descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:40
L16	3	15 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:40
L17	31	(@ad<"20021009" @rlad<"20021009") and DMA adj2 enable and DMA adj2 disable and DMA with register	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:45
L18	0	17 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 22:45
S15	19	(@ad<"20031001" @rlad<"20031001") and "707"/\$.cds. and DMA with descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 12:54

S16	3	S15 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 12:54
S17	38	(@ad<"20031001" @rlad<"20031001") and "707"/\$.ccls. and DMA with mode	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:00
S18	38	S17 and "707"/\$.ccls.	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:01
S19	2	S17 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:01
S20	15	S18 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:01
S21	1	S20 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:01
S22	3062	(@ad<"20031001" @rlad<"20031001") and DMA with support	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:02
S23	53	(@ad<"20031001" @rlad<"20031001") and DMA with support with descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:02
S24	44	S23 and data adj structure	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:11
S25	2	S24 and DMA adj support	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:12

S26	695	(@ad<"20031001" @rlad<"20031001") and DMA adj2 enabl\$2	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:12
S27	9	S26 and ("707"/\$ 710/22- 22).cols.	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:13
S28	41	(@ad<"20031001" @rlad<"20031001") and DMA adj2 enabl\$2 adj2 register	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:14
S29	14	S28 and data adj structure	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:14
S30	13	S29 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:14
S31	3	(@ad<"20031001" @rlad<"20031001") and "707"/\$.ccds. and DMA with support with register	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:15
S33	232	(@ad<"20031001" @rlad<"20031001") and DMA with support with register	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:15
S34	108	S33 and ("710"/\$ "707"/\$). cols.	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:16
S35	40	S34 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:16
S36	19	S35 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:16

S37	0	(@ad<"20031001" @rlad<"20031001") and DMA adj support adj (register descriptor)	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:21
S38	0	(@ad<"20031001" @rlad<"20031001") and DMA adj support adj descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:21
S39	0	(@ad<"20031001" @rlad<"20031001") and DMA adj support adj register	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:21
S40	36	(@ad<"20031001" @rlad<"20031001") and DMA adj support and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:22
S41	19	S40 and data adj structure and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:22
S42	39	(@ad<"20031001" @rlad<"20031001") and DMA adj disabl\$3	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:23
S43	10	S42 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:23
S44	490	(@ad<"20031001" @rlad<"20031001") and DMA adj capab\$5	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:24
S45	22	S44 and USB and data adj structure	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:25
S46	9	S44 and USB and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:25

S47	18	S45 not S46	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:25
S48	34	(@ad<"20031001" @rlad<"20031001") and DMA adj capab\$5 with register	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:27
S49	1	S48 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:27
S50	0	S48 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:27
S51	3	(@ad<"20031001" @rlad<"20031001") and DMA adj disable\$2 with register	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:32
S52	451	(@ad<"20031001" @rlad<"20031001") and DMA with data adj structure	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:38
S53	18	S52 and descriptor and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:39
S54	254	(@ad<"20031001" @rlad<"20031001") and DMA adj descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:54
S55	12	S54 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:54
S56	6	(@ad<"20031001" @rlad<"20031001") and recogniz\$3 with DMA with support	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:57

S57	27	(@ad<"20031001" @rlad<"20031001") and indicatS3 with DMA with support	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:58
S58	6	S57 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 13:58
S59	11	10/302009	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:01
S60	4	(@ad<"20031001" @rlad<"20031001") and no adj dma adj support	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:04
S61	4	("2004/0103234").URPN.	USPAT	OR	ON	2008/02/26 14:08
S62	0	"7708318".pn. and DMA	USPAT	OR	ON	2008/02/26 14:09
S63	2	S61 and DMA	USPAT	OR	ON	2008/02/26 14:09
S64	475	(@ad<"20021009" @rlad<"20021009") and DMA adj register	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:17
S65	98	S64 and data adj structure	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:17
S66	34	S65 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:18
S67	7	S66 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:18
S68	18	("20010049755" "20020052987" "20040036911" "5655145" "5845151" "6216183" "6298396" "6598097" "7000035").PN.	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:25

S69	12	S68 and DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:25
S70	9	(@ad<"20021009" @rlad<"20021009") and DMA adj device adj support	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:27
S71	0	(@ad<"20021009" @rlad<"20021009") and (peripheral device card) with DMA adj support with register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:27
S72	32	(@ad<"20021009" @rlad<"20021009") and (peripheral device card) with DMA adj support	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:27
S73	0	(@ad<"20021009" @rlad<"20021009") and (peripheral device card) with DMA adj support with indicat \$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:28
S74	2291	(@ad<"20021009" @rlad<"20021009") and DMA with detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:37
S76	461	(@ad<"20021009" @rlad<"20021009") and DMA adj3 detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:38
S77	92	S76 and descriptor and data adj structure	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:38
S78	74	S77 and serial	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:38
S79	29	S77 and USB	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:38

S80	1328	(@ad<"20021009" @rlad<"20021009") and DMA with capable	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:41
S81	293	(@ad<"20021009" @rlad<"20021009") and detect adj3 (DMA direct adj memory adj access)	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:43
S82	90	S81 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:44
S83	58	S82 and data adj structure	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:44
S84	25	S83 and USB	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:44
S85	2	(@ad<"20021009" @rlad<"20021009") and detect adj3 DMA adj3 (capable enable)	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:45
S86	96	(@ad<"20021009" @rlad<"20021009") and DMA adj3 compatib\$5	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:48
S87	7	S86 and descriptor	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:49
S88	2	(@ad<"20021009" @rlad<"20021009") and DMA adj3 unsupport\$3	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:51
S89	25	(@ad<"20021009" @rlad<"20021009") and PIO adj mode adj ("4" four)	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:52

S90	4	S89 and 710/22-27.cds.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:52
S91	3	(@ad<"20021009" @rlad<"20021009") and PIO adj mode and DMA adj3 disable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 14:54
S92	1	(@ad<"20021009" @rlad<"20021009") and DMA adj enable and DMA adj disable and descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 15:03
S93	243	(@ad<"20021009" @rlad<"20021009") and USB with descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 15:05
S94	78	S93 and data adj structure	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 15:05
S95	36	(@ad<"20021009" @rlad<"20021009") and determine with USB with support	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 15:10
S96	57	(@ad<"20021009" @rlad<"20021009") and card with DMA with support	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 16:48
S97	17	S96 and DMA with disable\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 16:48
S98	0	S97 and descriptor and data adj structure	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 16:49
S99	6	S97 and descriptor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 16:49

S100	12	I/O adj card with direct adj memory adj access	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 16:54
S101	3	S100 and data adj structure	US_PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/26 16:55

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